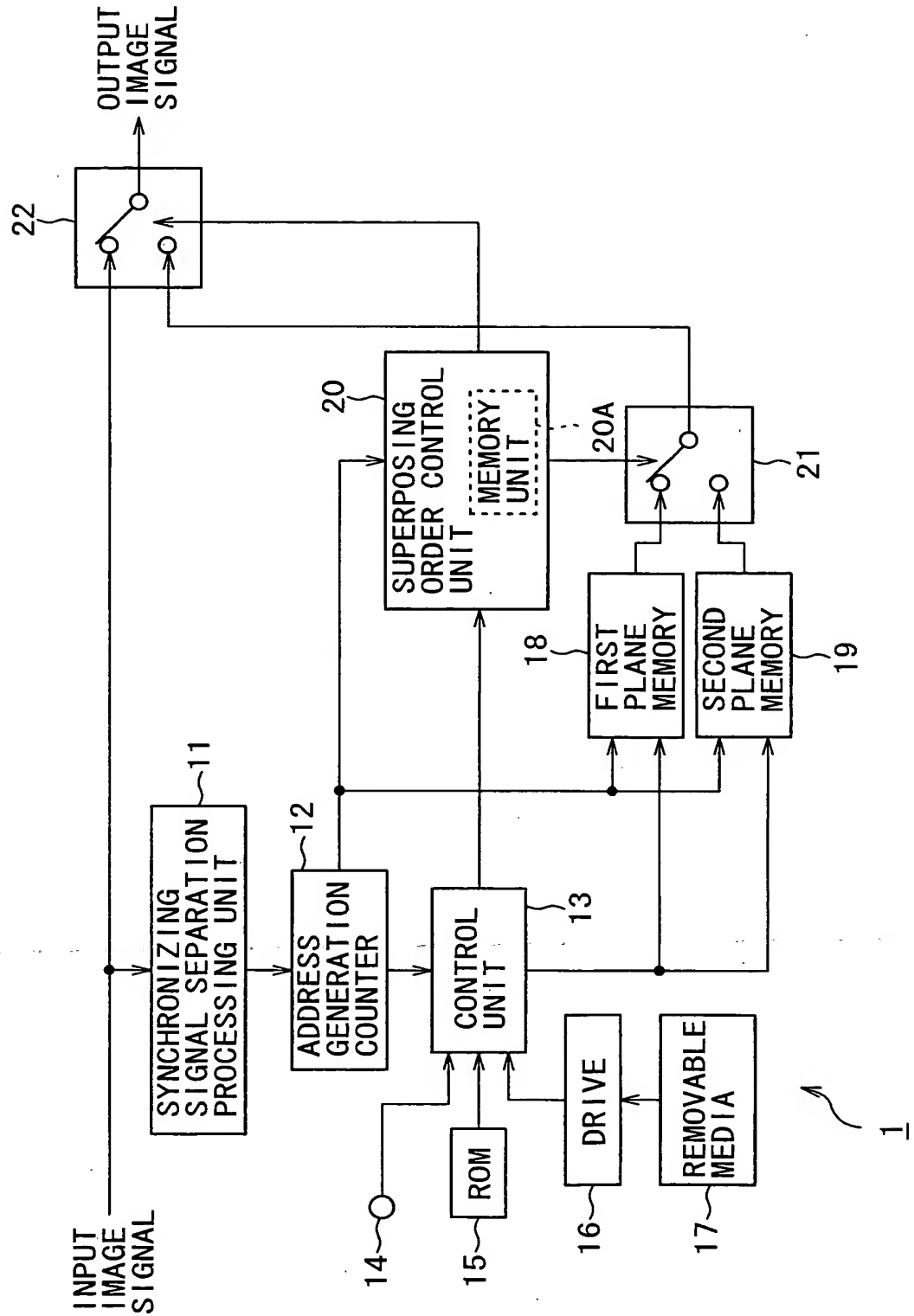


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FIG. 1



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FIG. 2A

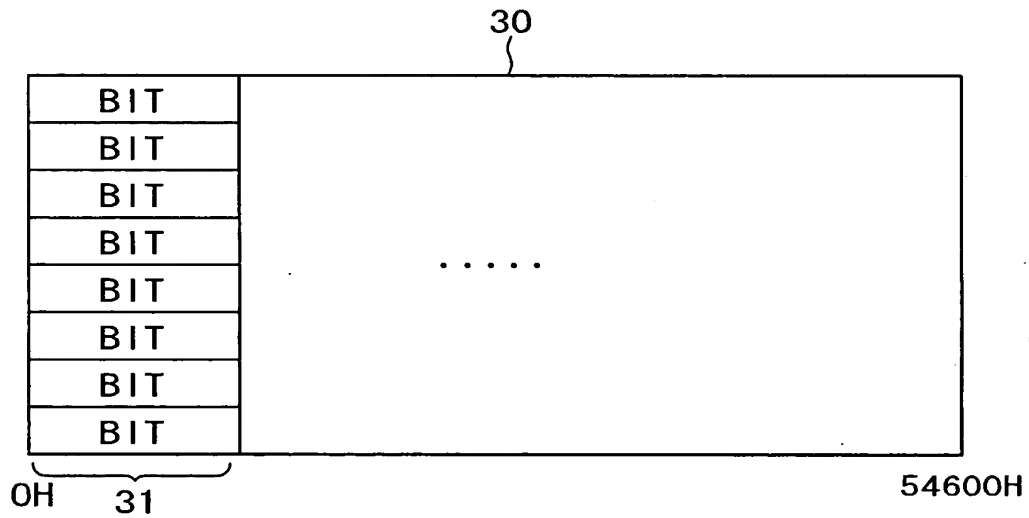


FIG. 2B

40 START ADDRESS	41 END ADDRESS	42 COLOR	44
a a a a	b b b b	c c c c	
⋮	⋮	⋮	

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FIG. 3A

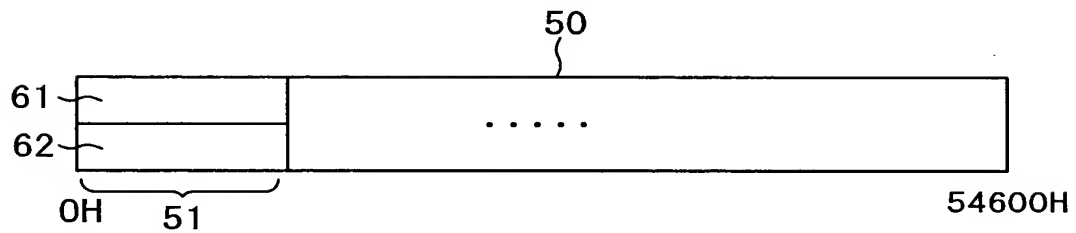
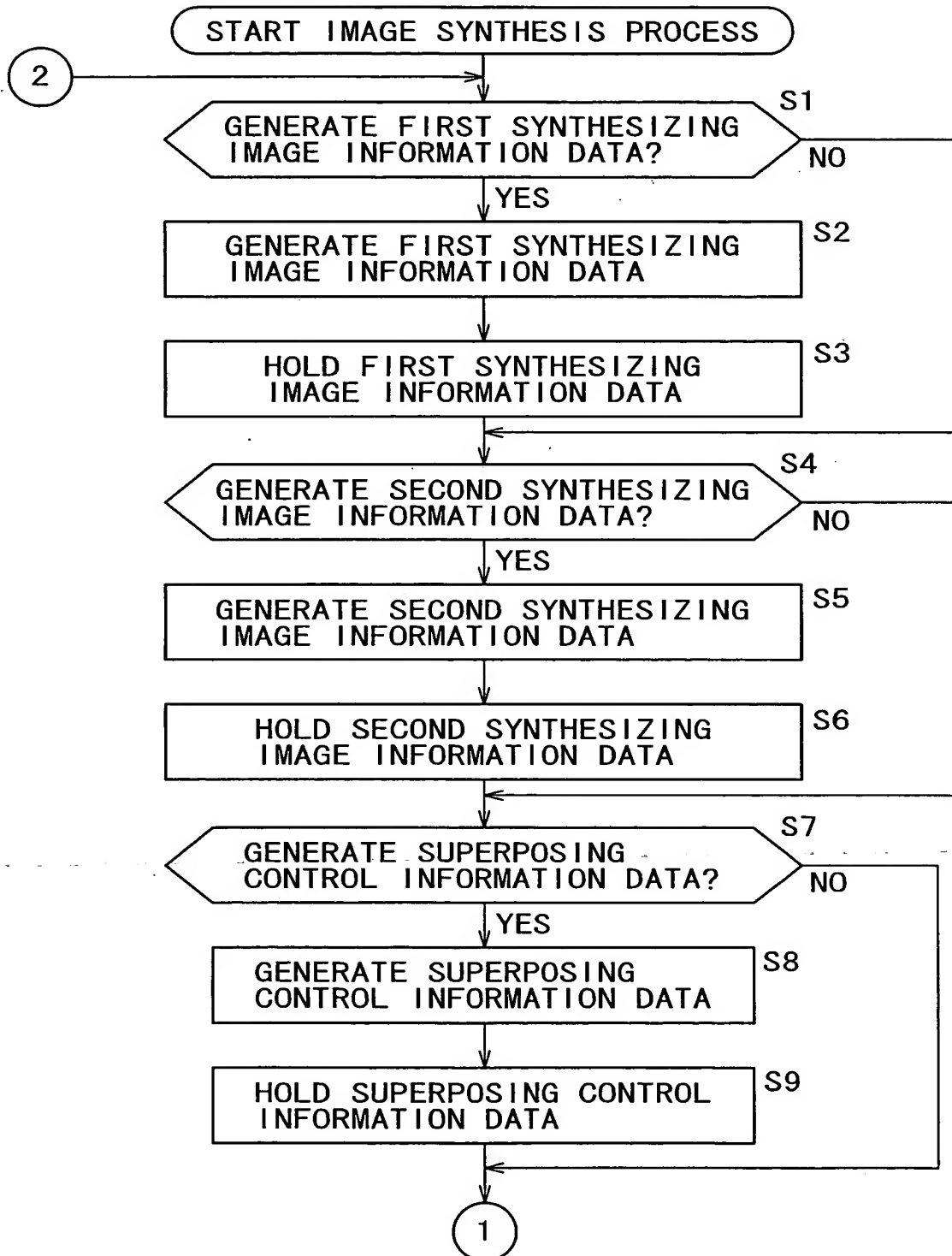


FIG. 3B

70 START ADDRESS	71 END ADDRESS	72 SELECTED SYNTHESIZING IMAGE INFORMATION DATA	73
a a a a	b b b b	FIRST PLANE MEMORY	} 81
⋮	⋮	⋮	

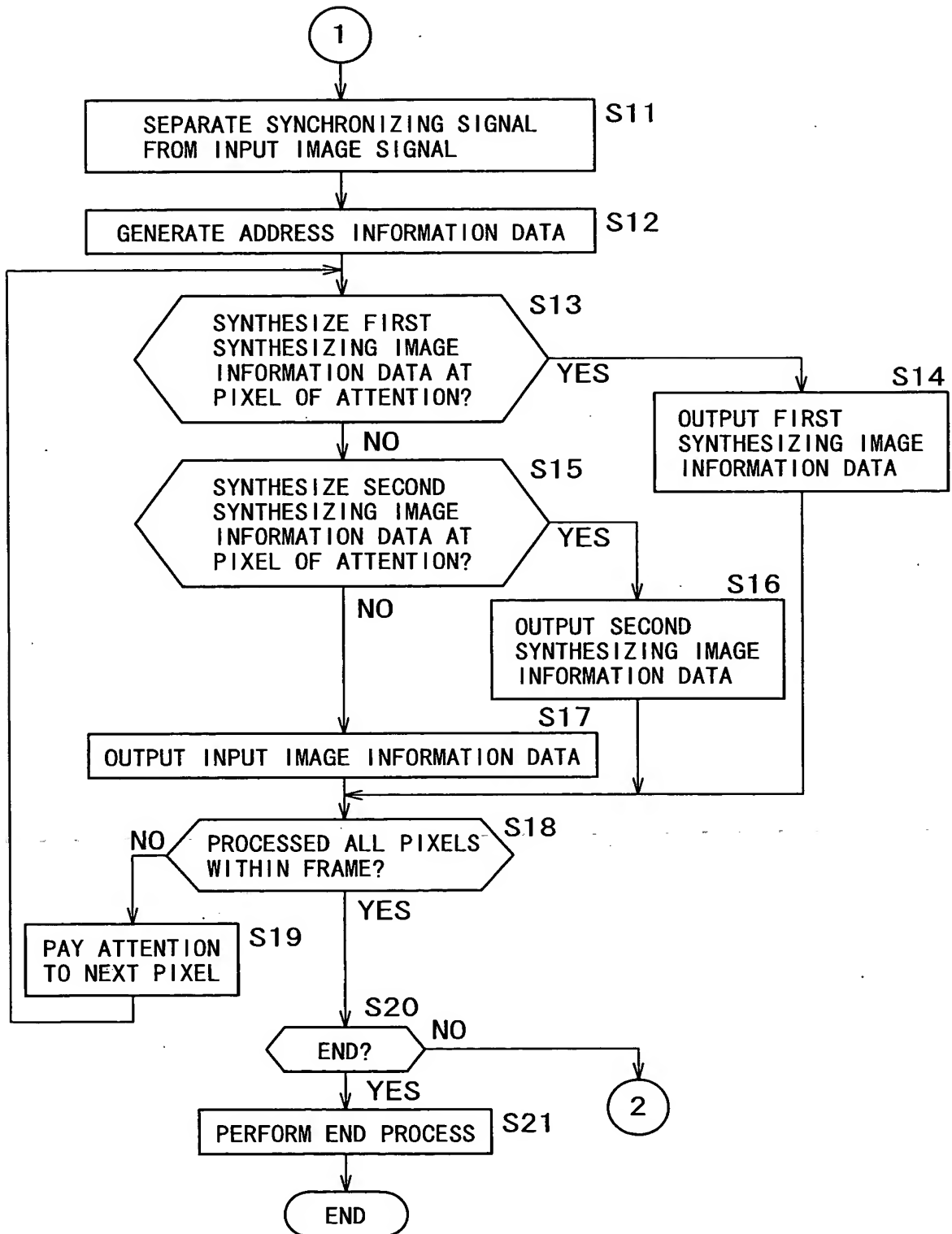
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FIG. 4



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FIG. 5



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FIG. 6A

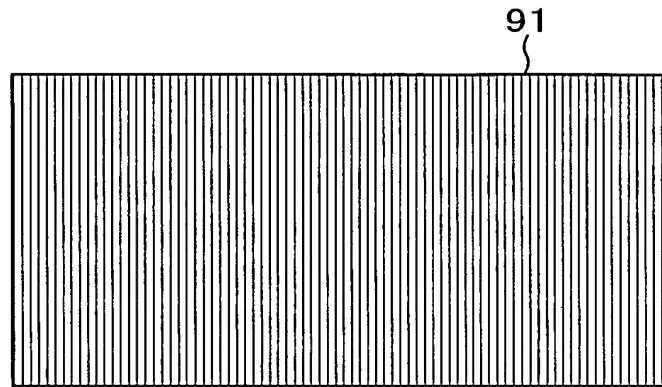


FIG. 6B

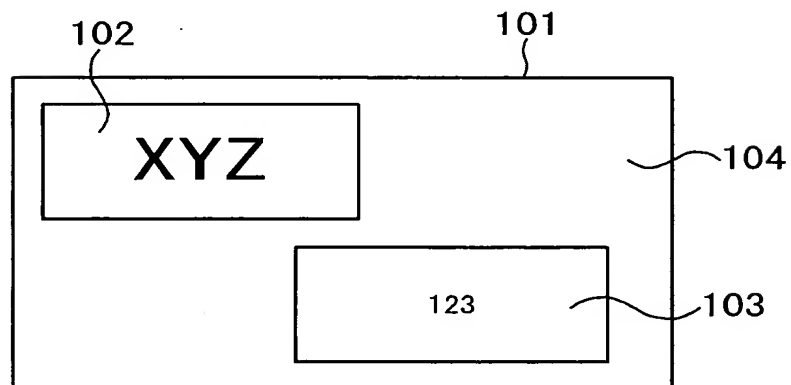
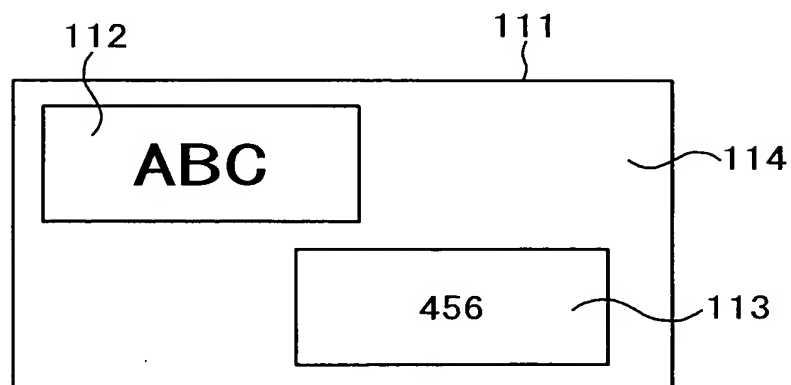


FIG. 6C



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FIG. 7A

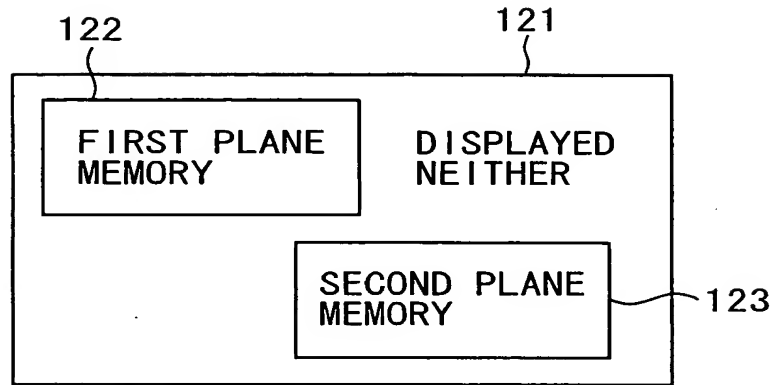


FIG. 7B

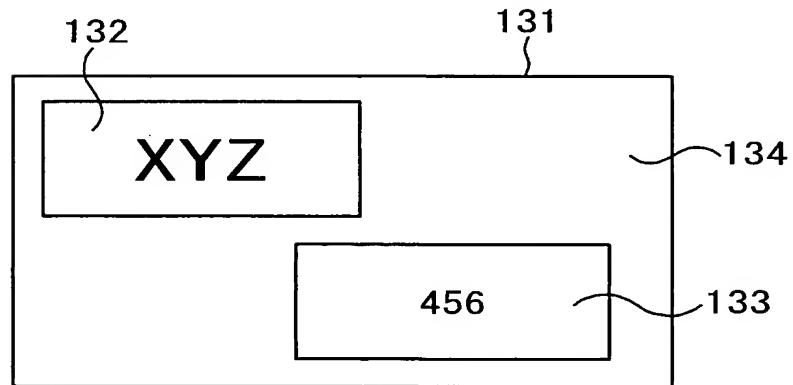
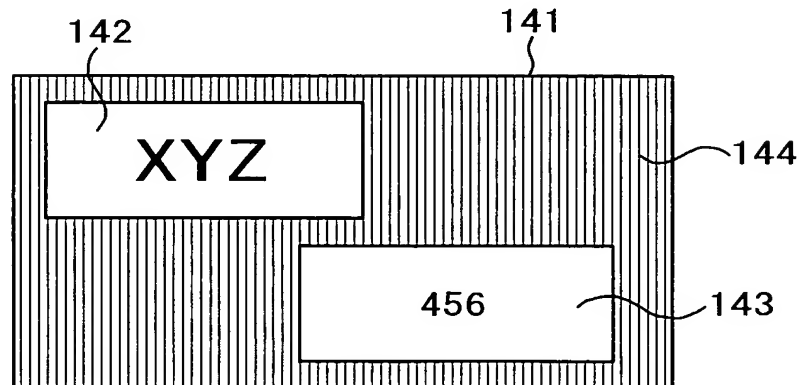
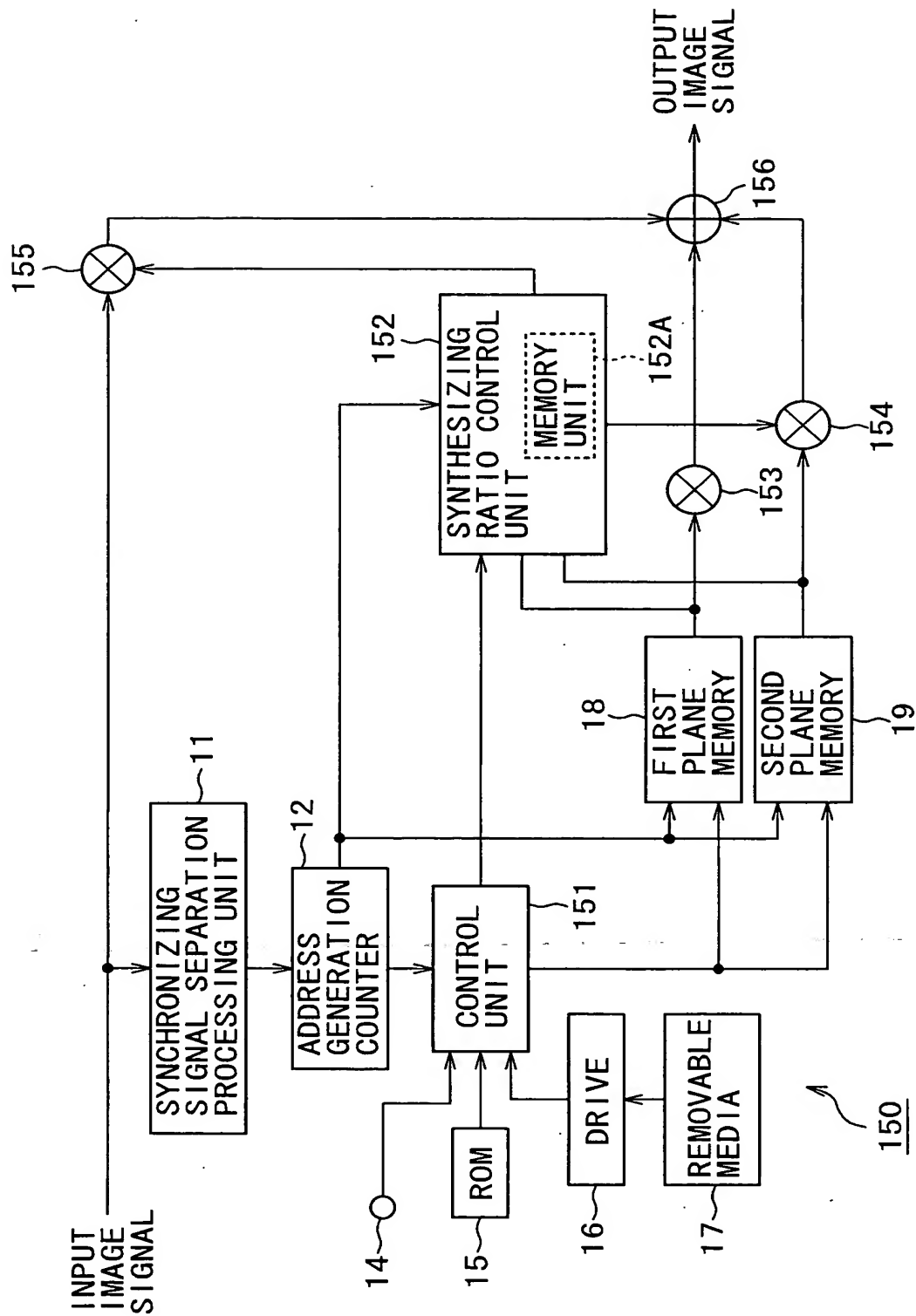


FIG. 7C



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FIG. 8



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FIG. 9A

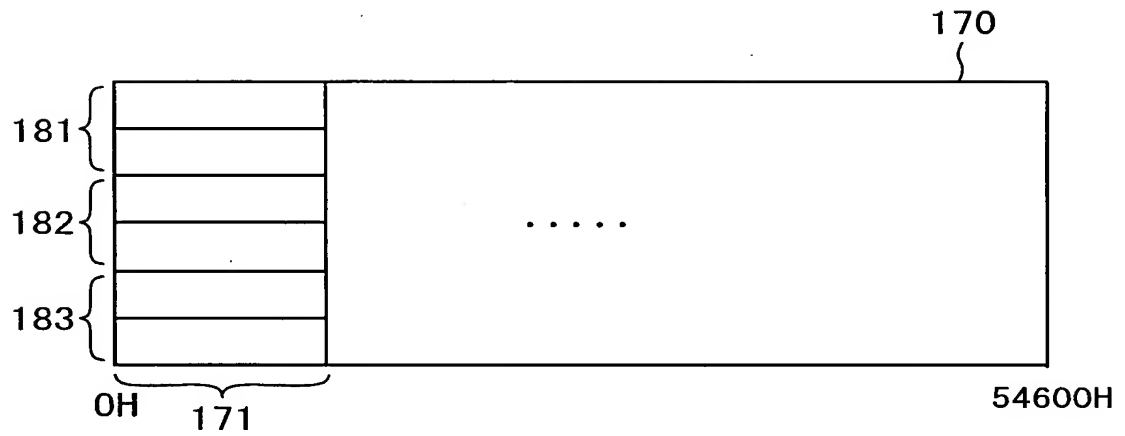


FIG. 9B

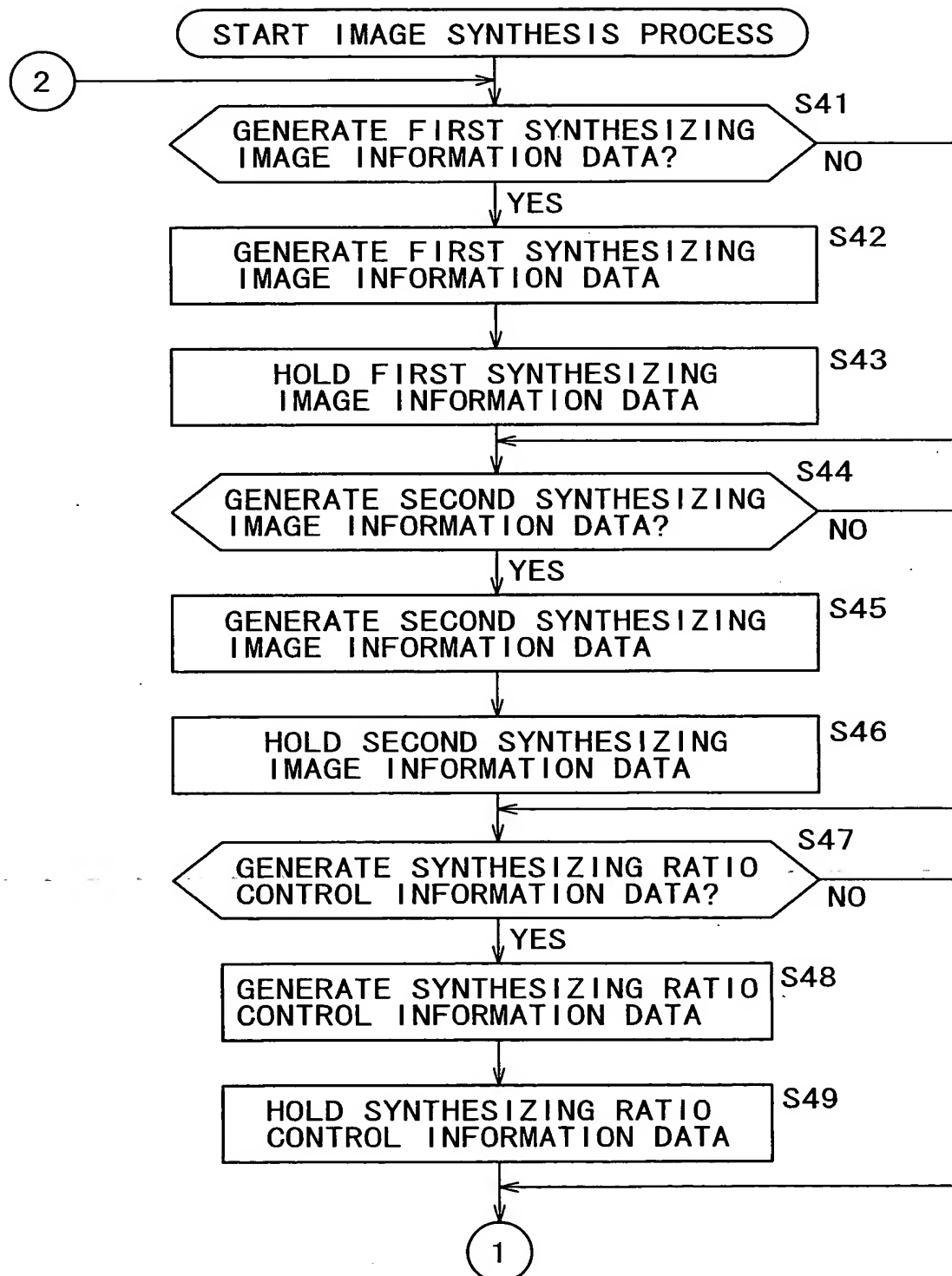
190 START ADDRESS	191 END ADDRESS	192 SYNTHESIZING RATIO INFORMATION DATA		
		193 FIRST PLANE MEMORY	SECOND PLANE MEMORY	193C INPUT IMAGE INFORMATION DATA
a a a a	b b b b	30%	30%	40%
⋮	⋮	⋮	⋮	⋮

193A 193B

} 201

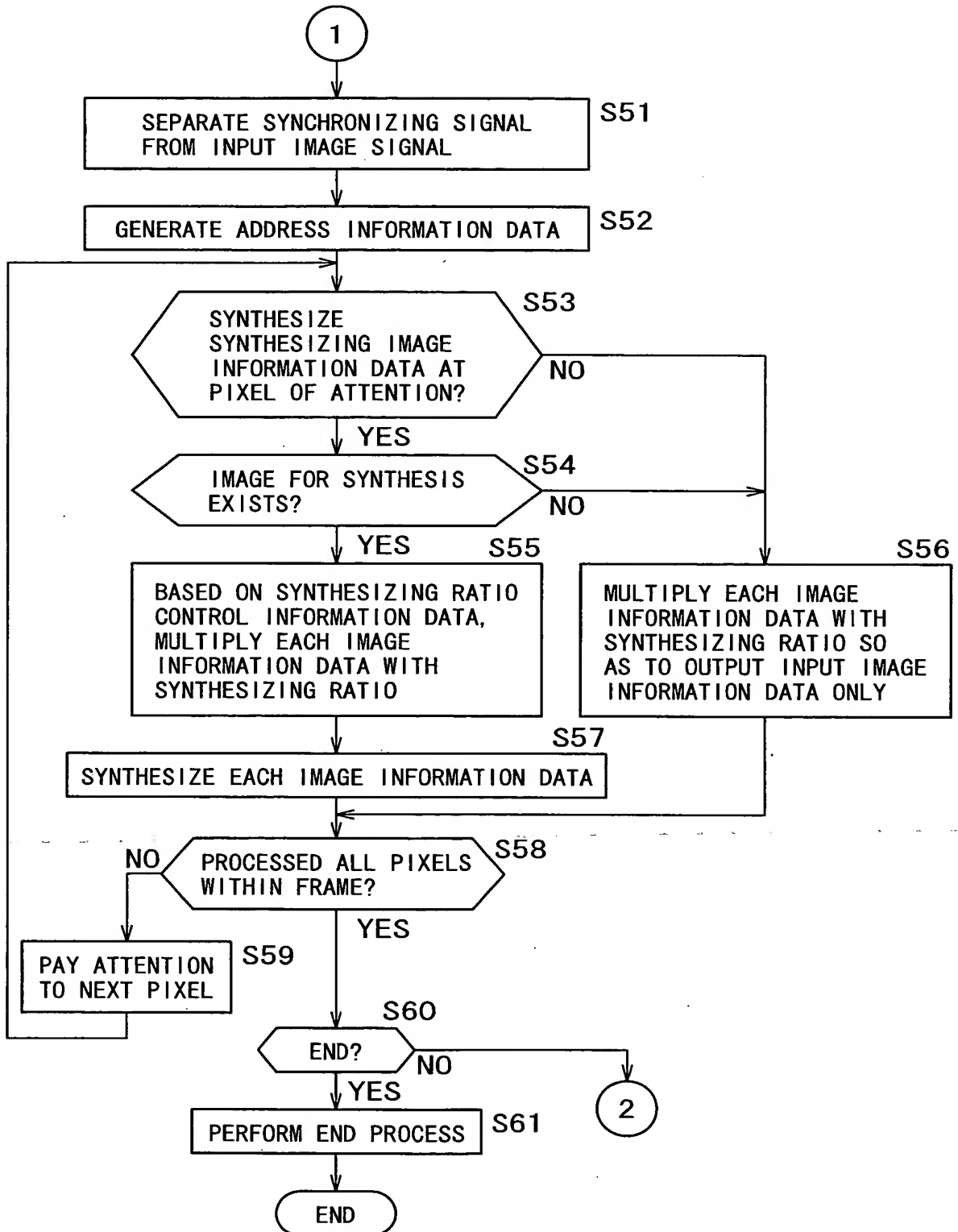
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FIG. 10



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FIG. 11



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FIG. 12A

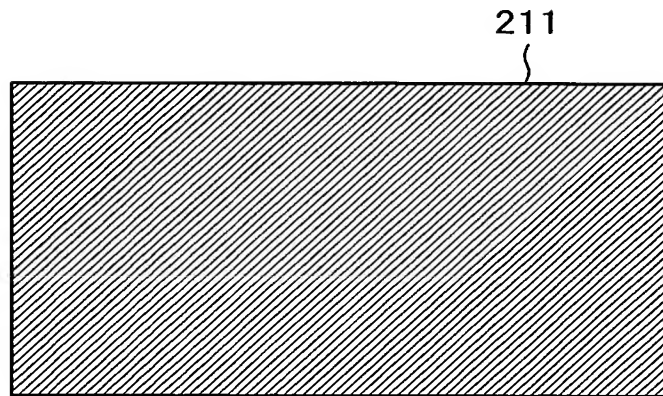


FIG. 12B

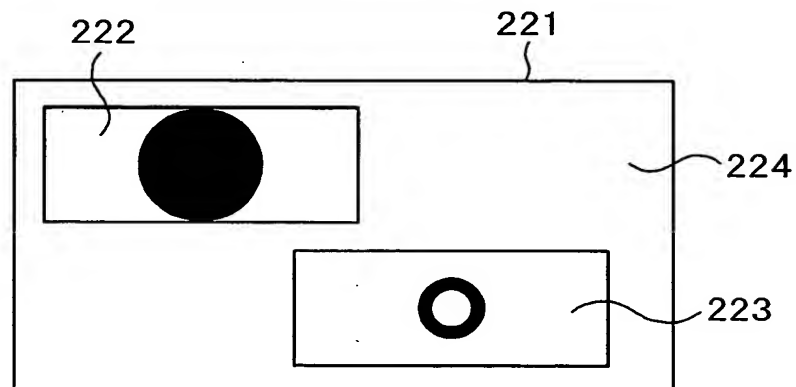
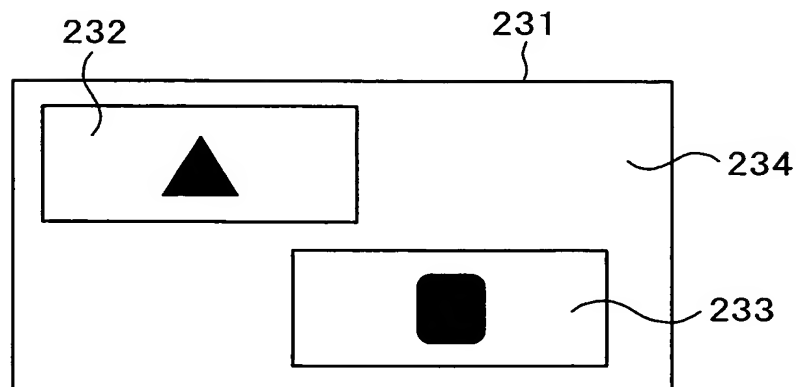


FIG. 12C



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FIG. 13A

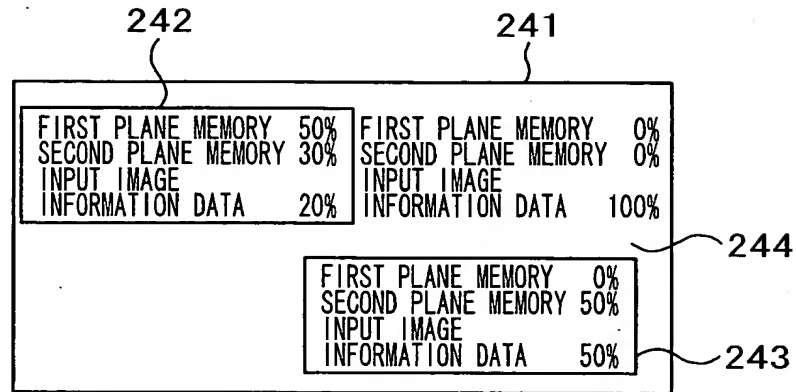


FIG. 13B

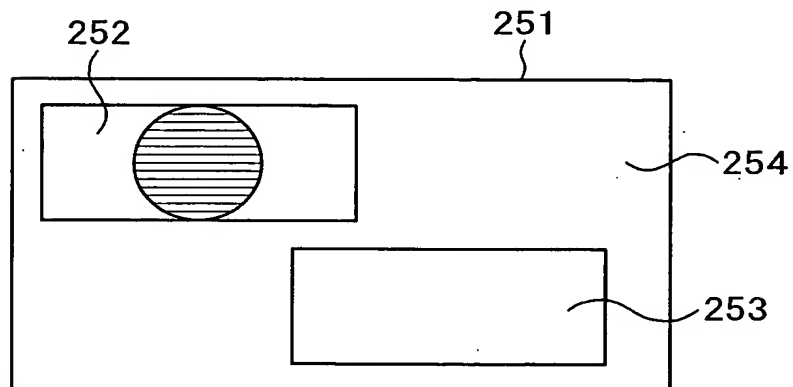


FIG. 13C

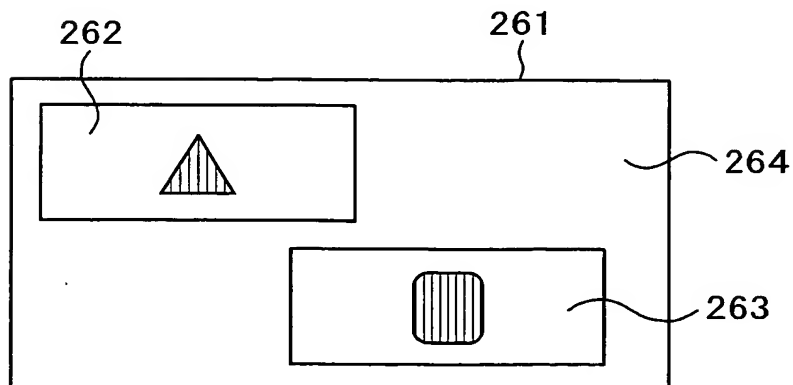


FIG. 14A

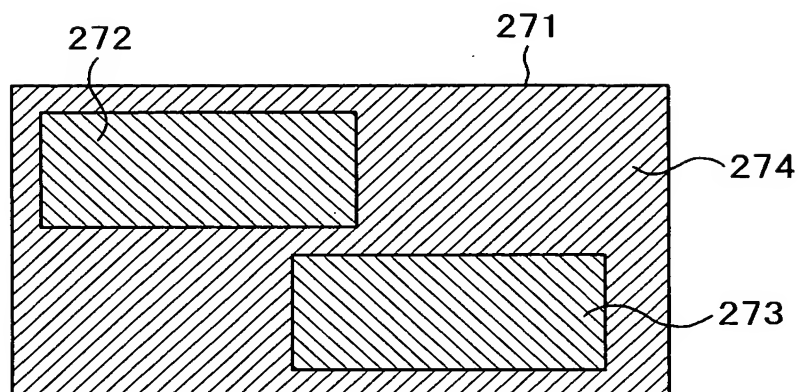
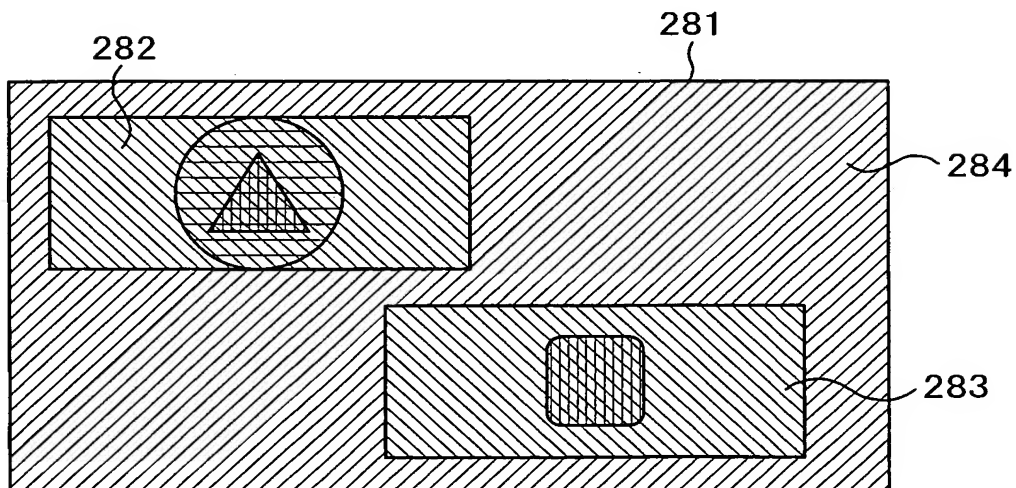


FIG. 14B



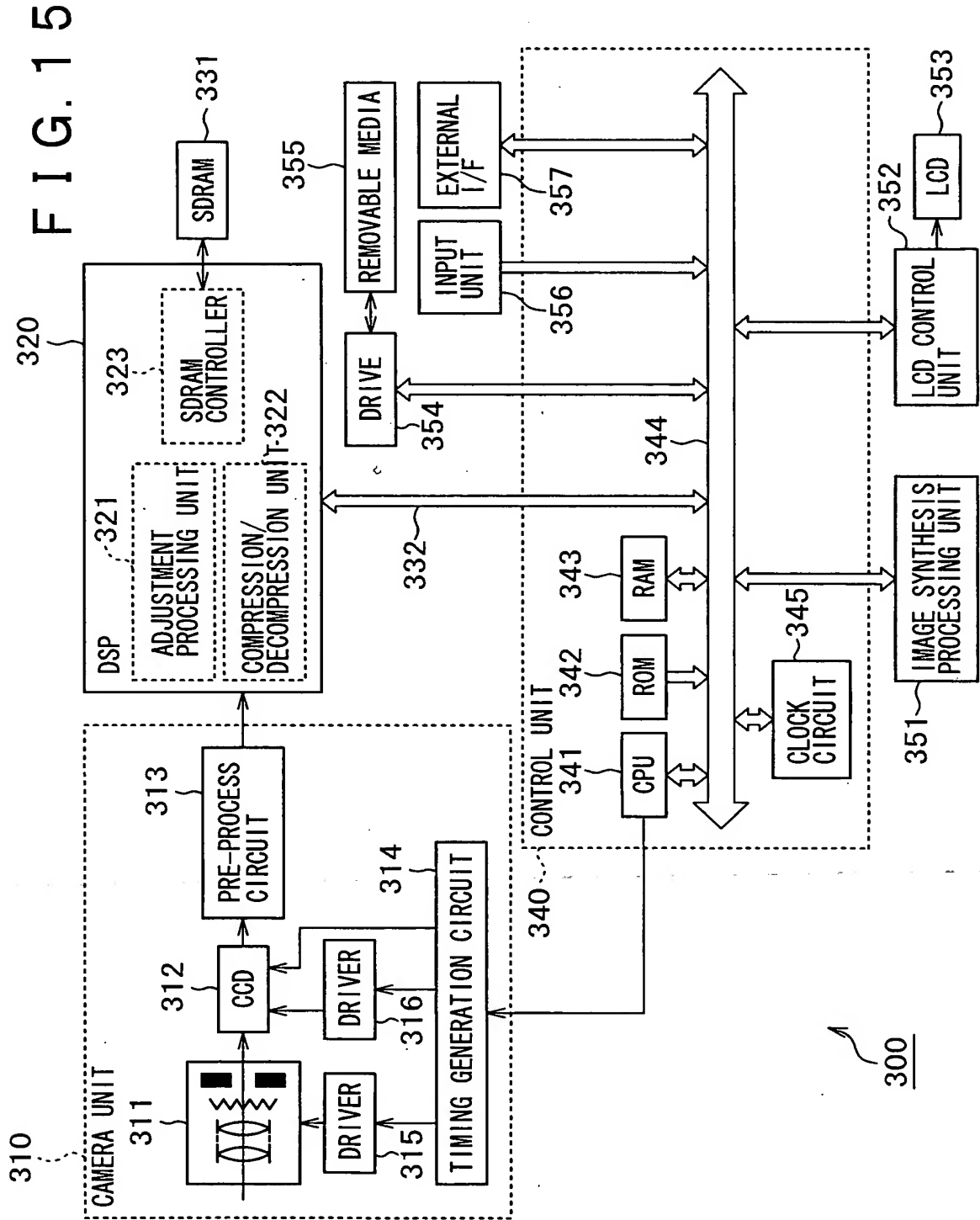


FIG. 16

